Homework 3

1. *Complex Impedance*

We derived a capacitor’s complex impedance in class. Find the amplitude of the capacitor’s impedance , and replace with .

(5)

1. is called reactance. Show your work below:
2. RC circuit
3. Simulate the circuit below, add voltage meters at VA and VB.

Insert your simulation here with VA VB values: (5)

A close up of a map

Description automatically generated

1. Fill the table below: (8) ( is and it shouldn’t change. is equivalent to )

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| input frequency Hz | Calculated | | | Multisim | |
| 10 | Xc | VB (V) | VA-VB (V) | VB (V) | VA-VB (V) |
| 100 |  |  |  |  |  |
| 500 |  |  |  |  |  |
| 1k |  |  |  |  |  |
| 1.2k |  |  |  |  |  |
| 1.3k |  |  |  |  |  |
| 1.4k |  |  |  |  |  |
| 1.5k |  |  |  |  |  |
| 1550 |  |  |  |  |  |
| 1585 |  |  |  |  |  |
| 1600 |  |  |  |  |  |
| 1.7k |  |  |  |  |  |
| 5k |  |  |  |  |  |
| 10k |  |  |  |  |  |
| 100k |  |  |  |  |  |

(2) show your work for

1. Filters
   1. The RC circuit we just built, is it a high pass filter or low pass filter?

(2)

* 1. Derive the formula for this filter and calculate the value at 500Hz. Does it agree with your table on page 2? Is ?

(10)

* 1. Derive the phase angle formula for this filter and calculate the value at 500Hz.

(8)

1. [Design Exercise] Cutoff frequency
   1. Design a new RC filter. Make sure its cutoff frequency is in the range of 100Hz to 1500Hz. Simulate this circuit’s output and input voltage. Click “Split” in Multisim, click run (the green triangle). Two sine waves will appear on your screen. Adjust the input frequency from 10 Hz to 50kHz. Observe the waves. Does the wave graph on the right of your screen agree with the numerical values showing on the meters\* on the left side of your screen? Now set the input frequency at .

(10) Insert your new RC filter’s schematic /graph **split view** screen shot here. Set input frequency at . Make sure both of the input and output sine waves are visible on the screen.

\*Note: Double click the voltage meter, on the right edge of your screen, check the box of “Periodic” in the “Interactive simulation” item. Now you will be able to see the peak values of VA and VB as you slide though different input frequencies.

* 1. Derive the cutoff frequency formula and calculate your filter circuit’s , voltage gain at , and phase angle at .

Show your calculation here. Compare your results with your simulation above. Move your mouse to the two (most likely one green one blue) sine waves or use cursors and find the two waves peak values and the phase angle. Do the measurements agree with your calculation? Is your filter a high pass filter or low pass filter?(10)

* 1. Make a Bode plot of this circuit, that is, the decibel voltage gain as a function of frequency in Hz. Mark your in your Bode plot. Is it at ?

(10) You may set the x-axis (f(Hz)) in the range of . [Here on our website](http://physics.wm.edu/~ran/Pages/fc.html#rs) you will find my examples of plotting in Python and Dr. Mikhailov’s plotting code in MATLAB. Mathematica can also do this job well.